	Enrolln	nent No:		Exam Seat No: H UNIVERSITY	-					
Summer Examination-2018										
Subject Name: Integrated Circuits & Applications										
	Subject	t Code: 4TE04IC	CA1	Branch: B.Tech (Electrical)						
	Semest	er: 4 Da	nte: 26/04/2018	8 Time: 10:30 To 01:30 Marks: 70)					
	(2) (3)	Use of Programn Instructions writt	ten on main ans ams and figures	r & any other electronic instrument is prohibited. swer book are strictly to be obeyed. (if necessary) at right places.						
Q-1		Attempt the fo	ollowing questi	ions:	(14)					
	1)	Draw the pin d	liagram of 741 I	IC.						
	2)	Draw the ideal	transfer curve	of an operational amplifier.						
	3)	The gain of a non-inverting amplifier is always less than 1. Determine whether								
		the given states	ment is true or f	false.						
	4)	Give any four i	non-linear appli	ications of operational amplifier.						
	5)	Which type of	feedback is app	olied in order to operate op-amp as an oscillator?						
	6)	The input impe	edance of a volt	tage follower circuit is very(High/Low).						
	7)	Give any four of	characterisites of	of an ideal-opamp.						
	8)	Draw the ideal	gain vs frequer	ncy response of band pass filter.						
	9)	A certain inver-		has R_i of 1 $k\Omega$ and R_f of 100 $k\Omega.$ find the closed-						
	10)	What is the eff	fect of negative	feedback on the bandwidth of an amplifier?						
	11)	Which amplific	er can be used t	to calibrate the physical input quantity to electrical						
		output quantity								
	12)	The gain of vo	oltage follower	circuit is						
	13)	Give any two a	applications of v	voltage to current converter.						



Give any two applications of instrumentation amplifier.

14)

Attempt any four questions from Q-2 to Q-8

Q-2 Attempt all questions

(14)

- Derive the equation of gain $A = 1 + \frac{R_F}{R_1}$ for a non-inverting amplifier where R_F is the feedback resistance and R_1 is the input resistance of an amplifier.
- b) The 741 IC op-amp having the following parameter is connected as a non-inverting amplifier with $R_1 = 470\Omega$ and $R_F = 4.7 K\Omega$:

Open Loop Gain A = 400,000

Input Impedance = $33M\Omega$

Output Impedance $R_o = 60\Omega$

Unity Gain Bandwidth $f_o = 0.6MHz$

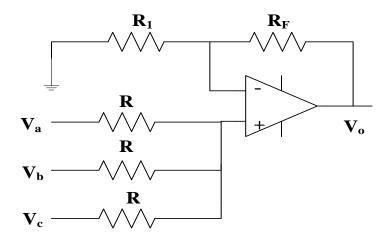
Compute: i) Gain with Feedback A_F

- ii) Input Impedance with Feedback R_{iF}
- iii) Output Impedance with Feedback R_{OF}
- iv) Bandwidth with Feedback $f_{\scriptscriptstyle F}$

Q-3 Attempt all questions

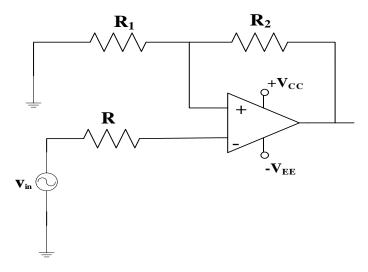
(14)

- a) Draw the circuit diagram of voltage follower circuit (unity gain amplifier) and
 Derive the following equation for voltage follower circuit.
 - a) Input Resistance with Feedback
- b) Output Resistance with Feedback
- b) The circuit shown in below figure is to be used as an averaging amplifier with the following specification: $V_a = V_b = 1.5V$, $V_c = 3V$, $R_1 = R = 1.5k\Omega$ and $V_o = 5.2V$. Determine the required value of R_F .





Q-4	a)	Attempt all questions Draw the circuit diagram of instrumentation amplifier using transducer bridge	(14) 07
		and explain how it can be used to measure the physical quantity.	
	b)	Draw the circuit diagram of differential amplifier with one op-amp and derive	07
		the following equations.	
		a) Closed Loop Voltage Gain b) Input Resistance with Feedback	
Q-5	a)	Attempt all questions Draw the circuit diagram of op-amp phase shift oscillator and explain its	
	,	operation with necessary conditions for oscillations.	07
	b)	Draw the circuit diagram of voltage to current converter with floating load and	07
		explain its operation.	
Q-6	a)	Attempt all questions Draw the circuit diagram and waveforms of op-amp based positive clipper circuit	(14) 07
		and explain its operation.	
	b)	Draw the circuit diagram of first order low pass filter and explain its operation.	07
Q-7	a)	Attempt all questions Draw the circuit diagram of first order high pass filter and explain its operation.	
	b)	In the circuit shown in below figure $R_1 = 150\Omega$, $R_2 = 68k\Omega$, $v_{in} = 500mV_{(p-p)}$	07
		sine wave, and saturation voltges = $\pm 14V$	



- i) Determine the thresold voltages $V_{\scriptscriptstyle ut}$ and $V_{\scriptscriptstyle lt}$
- ii) What is the value of hystersis voltage V_{hy} ?



Q-8	a)	Attempt all questions Draw the circuit diagram and waveforms of Astable multivibrator using 555		
		Timer IC and explain its operation.		
	b) Draw the high frequency equivalent circuit of an op-amp and explain that		07	
		loop voltage gain of an op-amp is a function of frequency.		

